Docket No.: 1572.1139

ABSTRACT OF DISCLOSURE

A method of controlling a computer having a plurality of memory buses adapted to be operated in a multi-channel mode by reading memory information of at least one of a plurality of memory modules connected to the respective memory buses and displaying whether the plurality of the memory buses can operate and/or are operating in the multi-channel mode by comparing the read memory information. Thus, a user can confirm whether a plurality of memory buses operate in a multi-channel mode.